

K.L.E.S Smt.C.I.MUNAVALLI POLYTECHNIC, HUBLI.
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
WORKLOAD FOR THE YEAR 2020-21 (ODD SEM)

S.No	Name of the Staff	Subject	Class	Strength	No.of Batch	Workload (Hours)		Work load (Credits)	Additional responsibility
						Theory	Practical		
1	Smt Sreelatha B HOD	1. Digital Electronics	3	60		4		4	
		2 P E	5	65		4		4	
		TOTAL							
2	Shri S M Wali SGL	1.DC-M/C –I	3	60		4		4	
		2 Est& Cost	5	65		4		4	
		3.Est Simulation lab	5	65	3		6*3=18	9	
		TOTAL							
3	Shri R R Kulkarni Lect	1.AE	3	60		4		4	
		2. Tra & Dis	5	65		4		4	
		3.PE Lab	3	65	3		6*3=18	9	
		TOTAL							
4	Chetan M Lect	1.BEPS	1	28		4		4	
		2. EPG	3	60		4		4	
		3. SGP	5	65		4		4	
		4. Prot Relay & Servicing lab	5	65	1		6*1=6	3	
		TOTAL							
5	Shri S F Savadi Foreman	1.Fund of Elect & Electronics Engg Lab	1	28	2		6*2=12	6	M/C Lab in Charge & Maintainance
		2. .DC-M/C –I Lab	3	60	3		6*3=18	9	
		TOTAL							
6	Shri.Jagadeesh A Inst	1. Prot Relay & Servicing lab	5	65	2		6*2=12	6	W/S&Electronics Lab in Charge & Mantainance
		2.A E Lab	3	60	3		6*3=18	9	
		TOTAL							
7	Shri. Anand V K Inst	1.Fund of Elect & Electronics Engg Lab	1	29+9(Arch & ID)	2		6*2=12	6	Computer Lab in Charge & Mantainance
		2.Circuit Sim lab	3	60	3		6*3=18	9	
		TOTAL							

HOD

PRINCIPAL

